

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

YONG-SOO KIM

Application No.:

Art Group:

Examiner:

Filed:

For: **Method For Forming Capacitor In  
Semiconductor Device**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**TRANSMITTAL OF FORMAL DRAWINGS**

Sir:

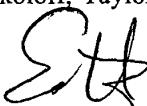
Enclosed herewith for filing in the above-identified U.S. Patent Application are the formal drawings, 5 sheets including 6 Figures. Please charge any additional fees or credit any overpayment to Deposit Account No. 02-2666. A duplicate copy of the Fee Transmittal is enclosed for this purpose.

Respectfully submitted,  
Blakely, Sokoloff, Taylor & Zafman LLP

Dated:

7/17/03

Eric S. Hyman, Reg. No. 30,139



12400 Wilshire Blvd., 7th Floor  
Los Angeles, California 90025  
Telephone: (310) 207-3800